

OK TO ENTER: /L.O./

03/24/2008

53168-500301D3

IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

Patent Application

RECEIVED  
CENTRAL FAX CENTER

FEB 26 2008

Inventor(s) Shrenik Deliwala

Case 53168-500301D3 Conf. No. 3947

Serial No. 10/076,920 Group Art Unit 2123

Filing Date February 15, 2002

Examiner Luke R. Osborne

Title Integrated Optical/Electronic Circuits and Associated  
Method of Simultaneous Generation ThereofCOMMISSIONER OF PATENTS  
WASHINGTON, D.C. 20231

SIR:

AMENDMENT

In response to the Office action dated December 26, 2007, please amend the above-identified application as follows: